

Open Position

Design and Verification Engineer

Simplicity analog
better efficiency
challenged inspired bit toggling
low power fascinating
creativity Nature less
resources Art passion
mixed-signal driving
services high
voltage industry IPs
sustainable analogy
satisfied leverage listen silicon
intensive-analog worldwide
specialty outstanding
excellence professional
innovative energy
efficiency quality

Canova Tech
Sculpting motivated
specialty
inspiration



Piazza Mario Saggin 2
35131 Padova, Italy

Via Operai 40/15
16149 Genova, Italy

+39 049 7811065
www.canovatech.com
hr@canovatech.com



Job Description

Canova Tech is growing, and fast! We are keen to hear from intermediate experienced Digital IC Design and Verification Engineers who would be interested to work with us in our design centers. As a Digital Design Engineer, you will drive and support feasibility, architecture definition, circuit implementation and verification of digital functions of our IP and Products, cooperating with Application Engineering, Analog Design team and IC Physical Design team.

Job Location

- Genova, Italy
- Padova, Italy

Requirements

In your new role you will:

- Define the architecture of digital blocks and their system level integration, in close cooperation with analog engineers and field application engineers.
- Be responsible for RTL design (Verilog, VHDL).
- Be responsible for the DFT concept.
- Support RTL2GDS flow, working closely with Physical designers.
- Drive and support the definition of the verification plan.
- Support the IP validation and debug activities.
- Provide documented compliance of delivered work products to performance and quality requirements.

Basic Qualifications:

- BSc/BA in Electronic/Electrical engineering or equivalent.
- 2+ years of relevant experience in digital design.
- Strong knowledge of hardware description languages (Verilog, SystemVerilog, VHDL).
- Experience in UVM Verification Environment is a strong plus.
- Familiarity with RTL2GDS flow is a plus.
- Understanding of analog and mixed-signal functions is a plus.
- Fluent English skills.
- Strong teamwork attitude.

About us

We deliver Standard and Custom IPs to the semiconductor industry worldwide.

We're specialized in combining the excellence of our Mixed Signal Design with the flexibility of our Digital and Software Capabilities to always achieve the best possible architectures and performances trade-off.

The respect of the project budget, the defined time to market and the agreed target quality is a cultural value of our company we're always offering to all our valuable customers.

If you love problem-solving and would like to get a true insight in to the world of semiconductor design you are just the one we are looking for.